



The 17<sup>th</sup> IEEE ICICDT 2019  
International Conference on IC Design & Technology  
Suzhou, China, June 17<sup>th</sup>– 19<sup>th</sup>, 2019  
<http://www.ICICDT.org>



The International Conference on IC Design and Technology (ICICDT) is the global forum for interaction and collaboration of IC design and technology for "Promoting scientific and technological progress, accelerating product time-to-market". Close collaboration of the multi-discipline technical fields design/device/process accelerates the implementation of new designs and new technologies into manufacturing. This conference has been organized by worldwide committee members from industry and academia since 2003. The meeting venue has been in Europe, North America and Asia for the past 16 years, and in 2019 the 17<sup>th</sup> IEEE ICICDT will be firstly held in mainland China. We extend our invitation to you to submit papers, present and discuss your works at the 17<sup>th</sup> IEEE ICICDT 2019 conference in this exciting field. The conference proceedings will be collected in IEEE publication database - IEEE X<sup>p</sup>lore and indexed by EI Compendex.

**2019 ICICDT Venue:**

The venue of 2019 International Conference on IC Design and Technology (ICICDT) will be at [Guanyuan Liusu Hotel Suzhou](#) (Originally known as "Suzhou Jinling Guanyuan International Hotel" ), situated by Suzhou's Dushu Lake (14 km from the walled historic town) and about 70 km from Shanghai Hongqiao international airport and about 120 km from Shanghai Pudong international airport.

**Contributed papers are solicited but not limited in the following subject areas:**

- Advanced materials and processing technologies, power semiconductor technologies and circuits
- Advanced transistor and interconnect structures
- Advanced packaging, vertical (2.5D/3D) integration
- Variation and fault-tolerant designs, reliability issues and mitigation approaches, EDA and design optimizations across system, circuit, and/or device levels for high performance, energy efficiency, yield, and/or reliability
- RF, analog, mixed signal, and I/O circuits for future technology generations
- Simulation and modeling of advanced processes, devices, and circuits
- Design for manufacturing, yield, and test, System-on-Chip (SoC) and System-in-Package (SiP) design integration
- Emerging technologies, circuits and Applications (MEMS, memory, Internet of Thing, Autonomous cars, Machine Learning, Artificial Intelligence)

Please submit 2-4 pages English paper in IEEE template (<http://www.icicdt.org/Author.html>). For paper submission and submission requirements, please click the website link: <http://www.icicdt.org/Submission.html>

**Paper Submission Deadline: March 15<sup>th</sup>, 2019**

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# The 17<sup>th</sup> IEEE ICICDT 2019

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